

## U.S. UTILITY Patent Application

PATENT NUMBER and  
ISSUE DATE

APPL NUM 10075113	FILING DATE 02/13/2002	CLASS 324	SUBCLASS 718	GAU 2133 2866	EXAMINER De Cady
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\*\*CONTINUING DATA VERIFIED:

\*\* FOREIGN APPLICATIONS VERIFIED:

FRANCE 0102333 02/21/2001

PG-PUB	DO NOT PUBLISH <input type="checkbox"/>	RESCIND <input type="checkbox"/>	
Foreign priority claimed 35 USC 119 conditions met		<input type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> yes <input type="checkbox"/> no	ATTORNEY DOCKET NO 00GR227754352
Verified and Acknowledged Examiner's initials			
TITLE : Method of testing a sequential access memory plane and a corresponding sequential access memory semiconductor device			
U.S. DEPT. OF COMM./PAT. & TM-PTO-436L(Rev. 12-94)			

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NOTICE OF ALLOWANCE MAILED		CLAIMS ALLOWED	
		Total Claims	Print Claim for O.G
Assistant Examiner		DRAWING	
		Sheets Drwg.	Figs.Drwg.
Primary Examiner		Print Fig.	
PREPARED FOR ISSUE		Application Examiner	
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